

TSMC-01-660

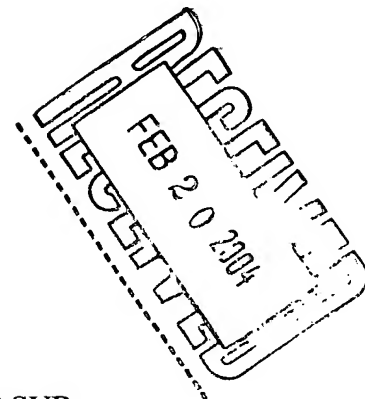
2812  
10/083,991

January 5, 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

FROM: George O. Saile, Reg. No. 19,572  
28 Davis Ave.  
Poughkeepsie, NY 12603

SUBJECT: Serial #: 10/083,991  
File Date: 02/26/2002  
Inventor: Chia-Der Chang  
Title: "PLANARIZATION METHOD FOR DEEP SUB  
MICRON SHALLOW TRENCH ISOLATION  
PROCESS"  
Art Unit: 2812  
Examiner: Stanetta D. Isaac



### RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action dated November 7, 2003 please consider  
the following arguments:

### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States  
Postal Service as first class mail in an envelope addressed to: Commissioner for Patents,  
P.O. Box 1450, Arlington, VA 22313-1450 on February 9, 2004.

Signature JCR

Date: 2/9/04

Name 2/9/04

**Amendments to the Specification:** There are no Amendments to the Specification in this paper.

**Amendments to the Claims:** There are no Amendments to the Claims in this paper.

**Amendments to the Drawings:** There are no Amendments to the Drawings in this paper.

**Remarks/Arguments:** Remarks/Arguments begin on page 3 of this paper.